IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ication of:

James H. Lie

Serial No.:

09/940,749

Title:

METHOD AND/OR ARCHITECTURE FOR GENERATING SUPERSET PINOUT FOR DEVICES WITH COMBINED PROGRAMMABLE LOGIC AND HIGH-SPEED

CHANNELS

Filed:

August 28, 2001

Attorney Docket No.: 0325.00503

Examiner:

Thompson, A.

Art Unit:

2825

In Response To:

Office Action mailed June 21, 2005

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Mail Stop - AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on <u>August 17, 2005</u>

AMENDMENT AFTER FINAL

Máil Stop - AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed June 21, 2005 please amend the above-identified application as follows: